



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

U.S. Patent Application of:

INVENTORS: Eichenberger et al.  
SERIAL NO.: 10/735,054 ART UNIT: N/A  
FILED: December 12, 2003 EXAMINER: Not yet assigned  
TITLE: Method and Apparatus for Eliminating the Need for Register Assignment,  
Allocation, Spilling and Re-Filling  
ATTORNEY DOCKET NO.: YOR920030382US1

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

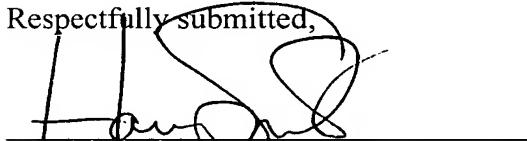
INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, copies are herewith submitted of documents which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists these documents.

The citation of these documents should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted,

  
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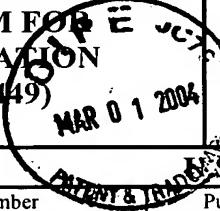


CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

2/25/04  
Date

Ann Okrentowich  
Ann Okrentowich

<b>INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)</b>		Docket No.: Y0R920030382US1	Serial No.: 10/735,054	
		Applicant(s): Eichenberger et al.		
		Filing Date: December 12, 2003	Group: N/A	
<b>U.S. PATENT DOCUMENTS</b>				
Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	
	US- US- US- US- US- US- US- US- US- US- US- US- US- US- US-			
<b>FOREIGN PATENT DOCUMENTS</b>				
Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
	- - - - - - - - - - - - - - - -			
<b>OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)</b>				
	KIYOHARA et al., "Register Connection: A New Approach to Adding Registers into Instructions Set Architectures", May 17-19, 1993, pgs. 1-10, The Symposium on Computer Architecture, San Diego, CA.			
Examiner's Signature:			Date Considered:	
Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered. Include a copy of this citation form with your next correspondence to the Applicant(s).				